

Questions for panel #1 (High Level Representations: DSLs, parallel languages, MPI+X, resilience):

- 1) What is the role of DSLs within Exascale and what impact does this have on vendors?
- 2) Will we see language extensions or new parallel languages from the Exascale program?
- 3) Where in the software stack is resilience best addressed; or need it be addressed uniformly everywhere?
- 4) What is the role of correctness in the development of Exascale software?
- 5) How fast will hardware change, how fast do we expect software and programming models to adapt? (the software stack?)